#### Machine performance improved with high-spe ed, high-precision, flexible motion control.

The evolution of the SYSMAC CS1 is accelerating advances in the production site.



Two Types of Outputs and Control of 1, 2, or 4 Axes Select from 1-axis, 2-axis, and 4-axis models with either open-collector output or line-driver output to suit a number of different applications.

#### A Variety of Positioning

A Variety of Positioning Functions There are 2 operating modes: direct operation (position, speed, acceleration, and deceleration data specified from the ladder program, which is effective for setting target positions, speeds, and acceleration rates immediately or during operation, and memory operation, where fixed patterns are stored beforehand in the Unit and used for operation. There are also a variety of positioning functions, such as interrupt feeding, which is effective for feeder control, and forced interrupt, which is useful in emergencies.

#### Advanced Motion Control Units

Easy System Construction Lasy system constitution by to 30 physical axes and two virtual axes, making a total of 32, can be controlled, and the servo intraface is handled by high-speed servo communications (MECHATROLINK-II, a communications (MECHATROLINK-II, a Electric Corporation). This makes it possible to control multiple axes with less wiring.

#### Easy Data Control

High-speed servo communications lets you read programs and parameter settings from CX-Programmer on a PC. You can also read and track the operating status of parameter settings inside the Servo Driver.

#### Easy Motion Control

otion control, including positioning, nchronizing (electronic gears, Motion contract, generating (electronic gears, electronic cams, tracking), speed, and torque control, can all be handled by the CS1. Eight motion tasks can be used for simultaneous motion program execution.

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#### Motion Control Units

Easy Programming with G Language and Multitasking The Motion Control Units use G language to ensure easy programming. The Units have a large programming capacity of up to 100 programs and 2,000 program blocks, and allow independent operation of 4 tasks.

#### High-speed Interlocks

Interrupt programs can be executed from the motion control program using Trom the motion control program using D codes (interrupt codes). Easy, fast interlocks ensure greater production efficiency. Synchronous control (electronic gears, electronic cams) is also possible.

 Customizable Counter Units A Whole New Concept A high-speed PLC with 20 I/O points, a 2-axis high-speed counter, and 2 pulse or analog outputs have all been combined into 1 Unit. The Customizable Counter Units allow easy execution of complicated applications.

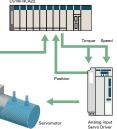
#### Customizable Counter Unit PLC 20 I/O po High-speed PLC Pulse ou overhead 0.1 ms



Analog output Analog input Easy Control for Bending and Pressing It is possible to switch between speed control and torque control from the ladder program, enabling bending operation for metals and pressing operation for bonding.



aad



## Synchronous Control with Electronic Cam

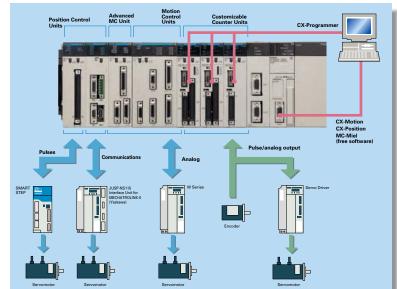
Electronic Cam Counter input and pulse output that previously could only be connected via a CPU Unit can now both be handled by the same Unit. The built-in high-speed PLC enables synchronic cams. The cam curve that determines the relationship between counter input and pulse output can be defined freely using the line-segment approximation function from the ladder program.

#### Design Costs Reduced by Modularization

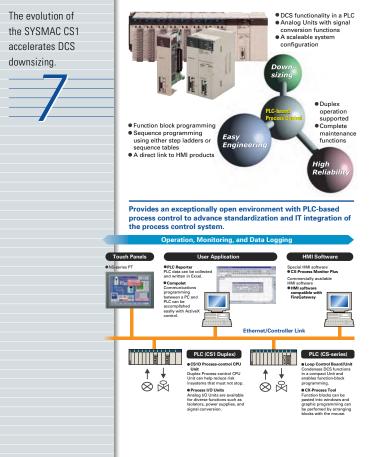
Modularization Ladder programs and I/O instructions to be re-used or shared by designers can be transferred from the main CPU Unit to the Units, allowing "modularization" that helps to reduce design costs. Up to 96 Units can be used, enabling easy system expansion in the future.

#### **Motion Applications with**

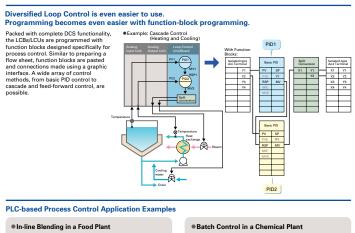
High-speed Response A wide range of interrupt functions and superior response performance enable motion applications requiring high-speed response using pulse I/O.

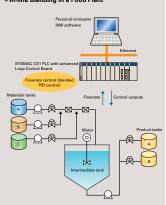


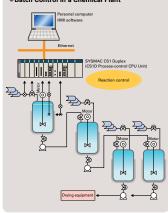
#### Smart Process Control OMRON PLC-based Process Control brings



#### **Major Innovations to Process Automation**





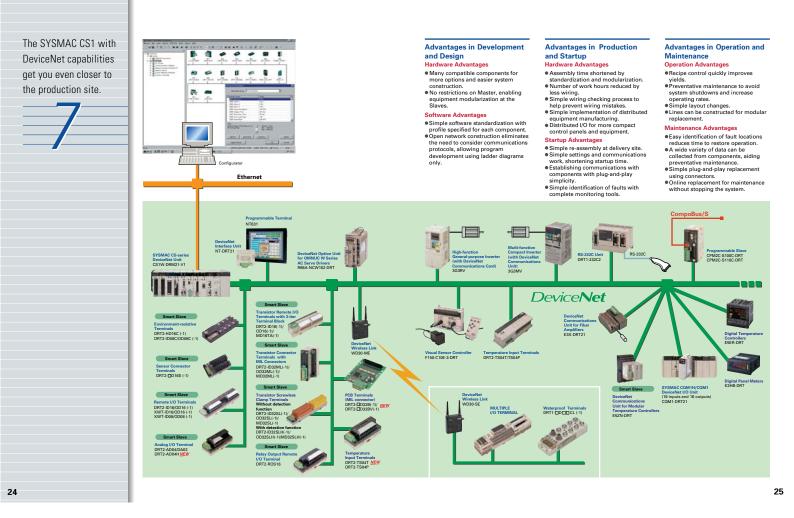


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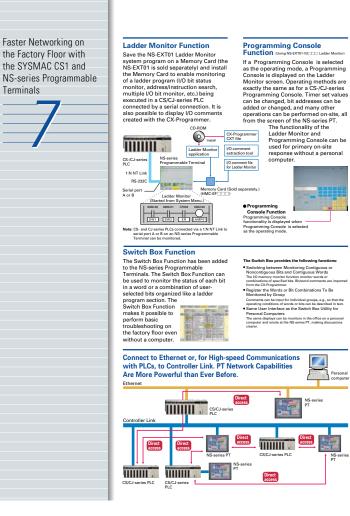
23

#### **DeviceNet Creates Many Advantages for D** for Production and Startup, and for Operat

## evelopment and Design, ion and Maintenance.



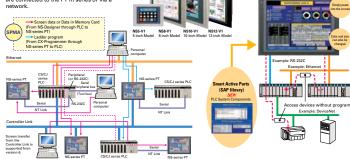
#### **Greater Compatibility with PLCs** Multilingual Globalization for Greater Mach ine Flexibility



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You may want to transfer screens to a PT through the PLC without changing computer connections or transfer a ladder program to the PLC through the PT by using the Ethernet or Controller Link. NS-series PTs provide Smart Active Parts (SAP library) enabling direct access to data in various devices

Ladder programs can be monitored or transferred from the CX-Programmer through the NS-series PT to PLCs that are connected to the PT in series or via network.



#### **Multilingual Version to Develop for Various Demands**

• Create Chinese or Korean screens on your Windows system. • Support multiple languages with the same screen data. • Create the source language labels and let suppliers handle the other languages. 

#### Multi-language Input with

Multi-language Input with Japanese Windows When Windows 2000 or XP is being used. Simplified Chinese, Traditional Chinese, Korean, and other language text can be input in MS-Designer. Sefect input a different Itanguage. You can also use this program together with RAKURAKU KANKOKUGO Chinese and Korean input systems) to convert Japanese to Chinese and Korean.



information on this software, ref site or send email to the followin //www.omronsoft.co.jp/SP/ fer to the following URL: http: E-maile and

NS Series: Easily Create Multilingual Screens or Your Windows System

#### Label Switching to Select from Multiple Languages

from Multiple Languages Up to 16 groups of labels (labels 0 to 15) can be registered for functional objects such as buttons, lamps, labels, and alarm settings, (Each label can correspond to a different language, for example, label 0 – Japanses, label 1 – Simplified Chinese, label 2 – Korean, label 3 = English, etc.) Once all of the labels have been input in each language with the multilingual input function, all of the labels can be switched to a different language at once just by specifying the corresponding label number from the PLC.



Property information for labels and other objects in screen data created using the NS-Designer can be exported to CSV files. These files can be edited in Excel and other programs. The screens can be created in the source language and then labels and other text exported to CSV files, which can be sent to translative for conversion to other translators for conversion to other languages. The translated CSV files can then be imported to automatically input the desired languages into labels.

Use Screen Import/Export

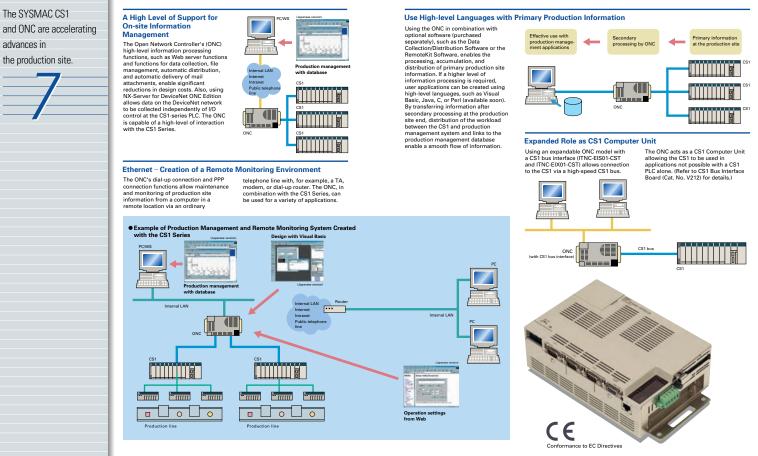
Functions to Separate Translation Work

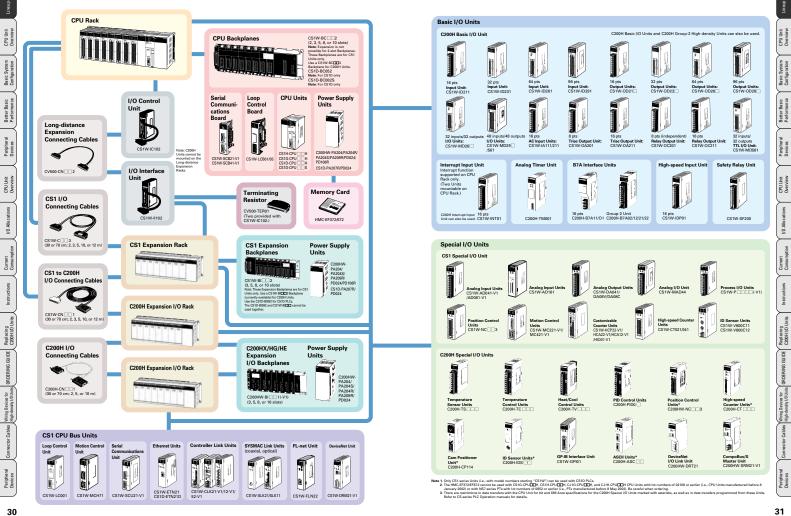
s PT



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## Dramatic improvements in on-site informati on management achieved with data collection functions.





### A Complete Lineup of Units for Optimum C ontrol.

#### CS1 CPU Unit Overview

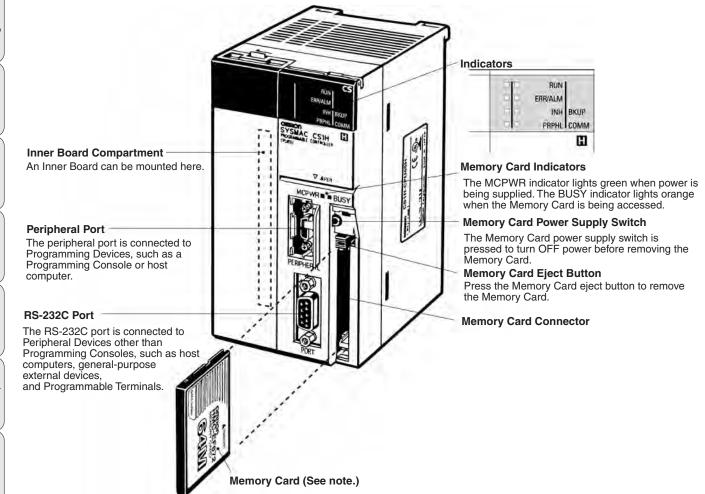
# CPU Unit Overview Basic System Configuration

Better Basic Performance

Peripheral Devices

Instructions Replacing C200H I/O Units





With the CS1 PLCs, Memory Cards and specified ranges of the EM Area can be used as file memory. File memory can be used to store the entire user program, I/O memory contents, and/or parameter area contents.

File memory	Memory type	Capacity	Model
Memory Cards	Flash memory	30 MB	HMC-EF372
		64 MB	HMC-EF672
EM File Memory EM area Bank 0 Bank n I Bank C Memory	RAM	EM Area capacity of CPU Unit (Max. capacity for CS1H-CPU67: 832 KB).	From the specified bank in the EM area of I/O memory to the last bank (specified in PC Setup).

Note: Memory Card Adapter: HMC-AP001 (The Memory Card Adapter can be used to mount Memory Cards in PC card slots to use the Cards on a personal computer.)

## **Specifications**

#### ■ CPU Units

Model	I/O bits	Program capacity	Data memory capacity (See Note.)	Instruction processing speed	Built-in ports	Options	CPU Unit
CS1H- CPU67H/CS1D- CPU67S	5,120 bits (Up to 7 Ex- pansion Racks)	250K steps	448K words	LD: 0.02 µs	Peripheral port and RS- 232C port.	Memory Cards Inner Boards, such as Serial Communications Board Only a Loop Control	Basic System
CS1H-CPU66H		120K steps	256K words				Sic S
CS1H- CPU65H/CS1D- CPU65S		60K steps	128K words			Board (CS1D-LCB05D) can be mounted in a CS1D CPU Unit. No oth-	Bas
CS1H-CPU64H		30K steps	64K words			er Inner Boards can be	sic
CS1H-CPU63H		20K steps				used.	etter Basic
CS1G-CPU45H	5,120 bits (Up to 7 Expansion Racks)	60K steps	128K words	LD: 0.04 µs			Better Basic
CS1G- CPU44H/CS1D- CPU44S	1,280 bits (Up to 3 Ex- pansion Racks)	Ex- 30K steps 64K words				eral	
CS1G-CPU43H	960 bits (Up to 2 Expan-	20K steps					Peripheral
CS1G- CPU42H/CS1D- CPU42S	sion Racks)	10K steps					Per
CS1D-CPU65H	5,120 bits (Up to 7 Ex-	60K steps	128K words	Basic instructions:	1		
CS1D-CPU67H	pansion Rácks)	250K steps	448K words	<ul> <li>0.02 μs min.</li> <li>Special instructions:</li> <li>0.04 μs</li> </ul>			CPU Unit

Note: The available data memory capacity is the sum of the Data Memory (DM) and the Extended Data Memory (EM).

Specifications

Lineup of Units

Peripheral Devices

I/O Allocations

Current Consumption

Instructions

Replacing C200H I/O Units

#### Common Specifications

Lineup of Units

The following 4 types of interrupt tasks are supported: Power OFF tasks:1 max., Scheduled interrupt 2 max., I/O interrupt tasks: 226 max.           Interrupt types         Scheduled Interrupts: Interrupts generated at a time scheduled by CPU Unit's built-in timer. I/O Interrupts: Interrupts from Interrupt Input Units. Power OFF Interrupts interrupts from Special I/O Units, CS1 Special Units, or Inner Board. Note: Interrupts: Interrupts from Special I/O Units, CS1 Special Units, or Inner Board. Note: Interrupts: Interrupts from Special I/O Units, CS1 Special Units, or Inner Board. Note: Interrupts cannot be used with a CS1D CPU Unit.           Function Blocks (See note 1.)         L/O Area         5,120 : CIO 000000 to CIO 031915 (320 words from CIO 00000 to CIO 0319 ) Setting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0999 used. I/O bits are allocated to Basic I/O Units, such as CS1 Basic I/O Units, C200H Basic I/O Units, and Group-2 High-density I/O Units.           Link Area         3,200 (200 words): CIO 150000 to CIO 139915 (words CIO 1000 to CIO 1399) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link § Special I/O Unit           Area         CS1 CPU Bus Unit 15,360 (960 words): CIO 150000 to CIO 139915 (words CIO 15000 to CIO 1899) CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (25 words per Unit, 16 Units Special I/O Unit           Area         1600 (100 words): CIO 300000 to CIO 295915 (words CIO 1900 to CIO 2959 ) Special I/O Unit 15,360 (960 words): CIO 300000 to CIO 295915 (words CIO 1900 to CIO 1999) Inner Board Area           Inner Board Area         1,600 (100 words): CIO 300000 to CIO 304915 (words CIO 1900 to CIO 1999 ) Inner Board Area         <	I	tem	Specification
Programming         Ladder diagram           Instruction length         110 7 steps per instruction           Ladder instructions         Approx. 400 (3-digit function codes)           Execution time         Basic instructions: 0.20 µs min., Special instructions: 0.04 µs min.           Number of tasks         288 (256 of which are also used as interrupt tasks: are supported. Power OFF tasks: Imax, Scheduled interrupt           Yeak         100 which are also used as interrupt tasks: Scheduled interrupt           Yeak         Scheduled Interrupts: Interrupts from Interrupt IopU Units           Yeak         100 Interrupts from Interrupt IOV Units power is turned OFF.           External I/O Interrupts: Interrupts from Special UNIts, or Inner Board.           Note: Interrupts is anterrupts tasks are abcoated tor use in function block definitions: Ladder programming language and str.           Function Blocks         Languages supported for use in function block definitions: Ladder programming language and str.           Function Blocks         Languages supported for use in function block definitions: Cadder programming language and str.           External I/O Oxfrac         5,120 : CIO 000000 to CIO 03915 (320 words from CIO 00000 to CIO 03919)           CIC (Core IO)         Krea         5,120 : CIO 000000 to CIO 139915 (words CIO 10000 to CIO 13991           Link Area         5,200 (200 words): CIO 100000 to CIO 139915 (words CIO 10000 to CIO 13891)           CIC (Core IO)	Control method		Stored program
Instruction length         10 7 steps per instruction           Ladder instructions         Approx. 400 (3-digit function codes)           Execution time         Basic instructions: 0.0.2 µs min., Special instructions: 0.0.4 µs min.           Number of tasks         288 (255 of which are also used as interrupt tasks)           Cyclic tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instruction         20.4 (21) instructions: 20.4 µs main.           Interrupt stasks are secured each cycle and are controlled with TKON(820) and TKOF(821) instruction         20.4 (21) instructions: 20.4 µs main.           Interrupt stasks are secured each cycle and are controlled with TKON(820) and TKOF(821) instruction         20.4 (21) instructions: 20.4 µs main.           Interrupts:         Interrupts: Interrupts executed when CPU Units, cost special units, or Inner Board.           Note:         Interrupts: Interrupts executed when CPU Units, cost special units, or Inner Board.           Note:         Interrupts: anon to be used with a CS1 D CPU Unit.           CIO (Core I/O) Area         5.120 : CIO 000000 to CIO 03191 (320 words from CIO 00000 to CIO 0319)           Area         Satting of first rack words can be changed from delault (CIO 00000 to CIO 0199)           Ink Area         3.200 (200 words): CIO 100000 to CIO 19915 (words CIO 1000 to CIO 1199)           Link Area         CS1 CPU Bus Unit is start allocated to CIS1 special I/O Units. (25 words per Unit, 16 Units social y 00) <t< td=""><td>I/O control metho</td><td>bd</td><td>Cyclic scan and immediate processing are both possible.</td></t<>	I/O control metho	bd	Cyclic scan and immediate processing are both possible.
Ladder instructions         Approx. 400 (3-digit function codes)           Execution time         Basic instructions: 0.02 µs min Special instructions: 0.04 µs min.           Number of tasks         288 (256 of which are also used as interrupt tasks)           Cyclic tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instructions: 0.04 µs min.           The following 4 types of interrupt tasks: as exported: Power OFF tasks:: 1max, Scheduled interrupt:           Interrupt types         Scheduled Interrupts: Interrupts generated at time scheduled by CPU Unit's built-in timer.           I/O Interrupts: Interrupts interrupts from Interrupt tasks: 256 max.         Special I/O Unit.           Function Blocks         External I/O Interrupts: Interrupts from Interrupt tasks         Special I/O Unit.           See total of Interrupts: Interrupts interrupts from CIO 0000 to CIO 0319 )         Nrea           Area         5,120 : CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319 )           Nrea         Special I/O Units. Such as CS1 Basic I/O Units. C200H Basic I/O Units, and Group-2 High-density I/O Units.           Link Area         3,200 (200 words): CIO 10000 to CIO 219915 (words CIO 1000 to CIO 1999 )           Link Area         3,200 (200 words): CIO 100000 to CIO 23915 (words CIO 2000 to CIO 1999 )           Link Area         3,200 (200 words): CIO 100000 to CIO 23915 (words CIO 1000 to CIO 1999 )           Link Area         3,200 (200 words): CIO 100000 to CIO 23915 (wor	Programming		Ladder diagram
Execution time         Basic instructions: 0.02 µs min., Special instructions: 0.04 µs min.           Number of tasks         288 (256 of which are also used as interrupt tasks)           Quicit tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instruction The following 4 types of interrupt tasks: 286 max.           Interrupt types         Scheduled Interrupts: Interrupts secuted when CPU Unit's power is turned OFF.           Exercise CPF Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: anot be used with a CS1 DCPU Unit.           Pice CIO Area         5,120 : CIO 000000 to CIO 031915 (320 words from CIO 00000 to CIO 0319)           Seting of first rack words can be changed from delault (CIO 00000 to CIO 0319)           Vea         2200 (200 words): CIO 10000 to CIO 119915 (words CIO 1500 to CIO 1999)           Link Area         2.200 (200 words): CIO 10000 to CIO 19915 (words CIO 1500 to CIO 1999)           CS1 CPU Bus Unit bis store operating status of CS1 CPU Bus Unit. Schwords per Unit, 15.300 (600 words): CIO 100000	nstruction length	ı	1 to 7 steps per instruction
Number of tasks         288 (256 of which are also used as inferrupt tasks)           Cyclic tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instruction that are used as inferrupt tasks: 256 max.           Interrupt types         Scheduled Interrupts: Scherupt tasks: 32 max., External Interrupt tasks: 256 max.           Scheduled Interrupts: Interrupts are supported. Power OFF tasks: 1 max., Scheduled interrupts: Interrupts from Interrupt tasks: 256 max.           Power OFF Interrupts: Interrupts from Special I/O Units, power is turned OFF.           Extended of Interrupts: Interrupts are not be used with a CS1D CPU Unit.           Col Core I/O         I/O Area           Scheduled of Gruss in function block definitions: Ladder programming language and str. text           The CIO Area         5.120 : CIO 00000 to CIO 031915 (320 words from CIO 0000 to CIO 0319)           See not 1.)         Setting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0399 used.           Inc CIO Area         3.200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 11991)           Interrupt         Special I/O Units.           Area         3.200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)           Area         15.300 (900 words): CIO 100000 to CIO 119915 (words CIO 1000 to CIO 1199)           Area         15.300 (900 words): CIO 100000 to CIO 119915 (words CIO 1090 to CIO 1999)           Area         15.300 (900 words): CIO 100000 to CI	_adder instructio	ns	Approx. 400 (3-digit function codes)
Cyclic tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instruction     The following 4 types of interrupt tasks: 32 max., External interrupt tasks: 256 max.     Scheduled interrupts: Interrupts generated at a time scheduled by CPU Unit's built-in timer.     I/O Interrupt itsaks: 32 max., External interrupt tasks: 256 max.     Scheduled interrupts: Interrupts generated at a time scheduled by CPU Unit's built-in timer.     I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's power OFF.     External I/O Interrupt: Interrupts executed when CPU Unit's Duilt-interrupt may.     Set 0.6 1.1     Interrupts: Interrupts executed when CPU Unit's Out OFPU Unit's Duilt-interrupt may.     Set 0.6 1.1     Interrupt is a contract of the set of CO 0000 to CIO 1000 to CIO 1199     Interrupt is are used of atal links and are allocated to Units in Controller Link Systems and PC Link S     Sepcial I/O Unit 15, 80e (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 1999)     Inter Board Area     Sopecial I/O Unit is are allocated to CI 1990 to CIO 1999.     Inner Board Area     Sopecial I/O Unit is are allocated to Inter maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of slots, however, is limited to 80 includir expansion slots. So maximum number of slots, however, is limited to 80 includir expansion slots. So maximum number of slots, however, is limited to 80 inc	Execution time		Basic instructions: 0.02 µs min., Special instructions: 0.04 µs min.
The following 4 types of interrupt tasks are supported: Power OFE tasks: 1 max., Scheduled interrupt     max. Scheduled Interrupts: Interrupts generated at a time scheduled by CPU Unit's built-in timer.     I/O Interrupts: Interrupts from Interrupt Indust: 256 max.     I/O Interrupts: Interrupts from Interrupt Indust: 256 max.     I/O Interrupts: Interrupts from Special I/O Unit's power is turned OFF.     External I/O Interrupts: Interrupts from Special I/O Unit's CS1 Special Units, or Inner Board.     Note: Interrupts from Special I/O Unit's CS1 Special Units, or Inner Board.     Note: Interrupts cannot be used with a CS1 D CPU Unit.     Section 1.)     I/O Area     S.120: CIO 00000 to CIO 03191 (320 words from CIO 0000 to CIO 0319)     Secting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0999     Used.     I/O D for are allocated to Basic I/O Units, such as CS1 Basic I/O Units, c200H Basic I/O Units, and     Group-2 High-density I/O Units.     S.200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)     Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S     Secial I/O Unit is store operating status of CS1 CPU Bus Units, (Se words per Unit, 16 Units     Special I/O Unit and C200H Special I/O Units and 2020H Special I/O Units are 2020H Special I/O Units are 2020H Special I/O Units are 2020H Special I/O Units and 2020H Special I/O Units and 2020H Special I/O Units are 2020H Special I/O Units and 2020H Special I/O Units and 2020H Special I/O Units and 2020H Special I/O Units are 2020H Special I/O Units and 2020H Special I/O Units are 2020H Special I/O Units and 2020H Special I/O Units are 2020H Special I	Number of tasks		288 (256 of which are also used as interrupt tasks)
2         max., LV 0 interrupt tasks: 32 max., External interrupt tasks: 25 max.           Interrupt types         Scheduled Interrupts: Interrupts generated at a time scheduled by CPU Unit's built-in timer. I/O Interrupts: Interrupts executed when CPU Unit's power OFF.           External 10 O Interrupts: Interrupts executed when CPU Unit's power OFF.         External 10 Interrupts: Interrupts executed when CPU Unit's power OFF.           Function Blocks         Languages supported for use in function block definitions: Ladder programming language and str. text           CIO (Core I/O) Area         I/O Area         5.120 : CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319)           Can be used as work bits if not used as shown here.)         I/O Area         5.120 : CIO 00000 to CIO 031915 (320 words from CIO 0000 to CIO 0319)           Link Area         3.200 (200 words): CIO 10000 to CIO 119915 (words CIO 10000 to CIO 1099) used.         I/O bits are allocated to Basic I/O Units, such as CS1 Basic I/O Units, and Group-2 High-density I/O Units.         Sci CPU Bus Unit Bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S pecial I/O Unit           Link Area         3.200 (200 words): CIO 150000 to CIO 199915 (words CIO 1900 to CIO 1999)         CIN CPU Bus Unit Bits are allocated to CIN special I/O Units and C2004 Special I/O Units. Soch aper Unit, 16 Units Area         CSI CPU Bus Unit Bits are allocated to CIN 199915 (words CIO 1900 to CIO 1999)           Inner Board Area         16.00 (100 words): CIO 100000 to CIO 199915 (words CIO 1900 to CIO 1999)         Inner Board Area			Cyclic tasks are executed each cycle and are controlled with TKON(820) and TKOF(821) instructions
VD Interrupts:         Interrupts from Interrupt Input Units.           Power OFF Interrupts:         Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts:         Interrupts composed I/O Units, CS1 Special Units, or Inner Board.           Note:         Interrupts cannot be used with a CS10 CPU Unit.           CIO (Core I/O)         I/O Area         5,120 : CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319)           Area         Setting of first rack words can be changed from default (CIO 0000) so that CIO 0099 used.           (The CIO Area are used as work bits if not used as shown here.)         Link Area         3,200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)           Link Area         3,3200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)         Link Kits are used for data links and are allocated to Units.           Special I/O Unit         5,400 (400 words): CIO 150000 to CIO 199915 (words CIO 1500 to CIO 1899)         CS1 CPU Bus Unit           Area         CS1 CPU Bus Unit bits are allocated to CIO 2925915 (words CIO 2000 to CIO 3959)           Area         Special I/O Unit         5,500 (960 words): CIO 150000 to CIO 199915 (words CIO 1000 to CIO 3999)           Area         1,500 (100 words): CIO 19000 to CIO 199915 (words CIO 3000 to CIO 3999)           Area         1,500 (100 words): CIO 19000 to CIO 393915 (words CIO 3000 to CIO 3999)           Area         1,500 (100 words): CIO 1900			2 max., I/O interrupt tasks: 32 max., External interrupt tasks: 256 max.
Power OFF Interrupts: Interrupts executed when CPU Unit's power is turned OFF.           External I/O Interrupts: Interrupts from Special I/O Units, CS1 Special Units, or Inner Board.           Note:: Interrupts: anterrupts from Special I/O Units, CS1 Special Units, or Inner Board.           See note 1.)         Iterrupts: cannot be used with CS1D CPU Unit.           CIO (Core I/O)         I/O Area         5,120 : CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319)           Area         Setting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0999 used.           CID ACO (Core I/O)         I/O Area         S200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)           Link Area         3.200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199)           Link bits are used for data links and are allocated to Units in CortOller Link Systems and PC Link S           Special I/O Unit         15,360 (960 words): CIO 10000 to CIO 199915 (words CIO 1000 to CIO 2995)           Special I/O Unit         15,360 (960 words): CIO 20000 to CIO 299515 (words CIO 2000 to CIO 2995)           Special I/O Unit         15,360 (960 words): CIO 20000 to CIO 299515 (words CIO 2000 to CIO 2995)           Inner Board Area         800 (50 words): CIO 30000 to CIO 39915 (words CIO 3000 to CIO 3994)           Area         SYSMAC BUS         800 (50 words): CIO 30000 to CIO 39915 (words CIO 3000 to CIO 3999)           Inner Board Area         800 (50 words): CIO 3000	Interrupt types		
External I/O Interrupts: Interrupts from Special I/O Units, CS1 Special Units, or Inner Board.           Function Blocks (See note 1.)         Languages supported for use in function block definitions: Ladder programming language and stru- text           CIO (Core I/O) Area         I/O Area         5,120: CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319) Area           CThe CIO Area can be used as work bits if not used as shown here.)         I/O bits are allocated to Basic I/O Units, such as CS1 Basic I/O Units, C200H Basic I/O Units, and Group-2 High-density I/O Units.           Link Area         3,200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S Special I/O Unit           Special I/O Unit         6,400 (400 words): CIO 10000 to CIO 189915 (words CIO 1000 to CIO 1899)           Area         Special I/O Unit bits are allocated to CS1 Special I/O Units. (25 words per Unit, 6 Units Special I/O Unit bits are allocated to CS1 Special I/O Units. (26 words per Unit, 6 Units max. The maximum number of IO1 words CIO 15090 to CIO 1999 ) Inner Board Area           Inner Board Area         1,600 (100 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3949)           SystMAC BUS         Bo0 (50 words): CIO 30000 to CIO 304915 (words CIO 3000 to CIO 3949 ) Inner Board Area           I/O Terminal bits are allocated to IO 100 Torminal Units (but not to Siave Racks) connected to SYSMA Remote I/O Master Units. (14 words): CIO 30000 to CIO 304915 (words CIO 3000 to CIO 30491) Inner Board Area           I/O Terminal bits are allocate			
Note:         Interrupts cannot be used with a CS1D CPU Unit.           Function Blocks (See note 1.)         Languages supported for use in function block definitions: Ladder programming language and str. text           CIO (Core I/O) Area         5,120 : CIO 00000 to CIO 031915 (320 words from CIO 0000 to CIO 0319) Setting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0399 used.           CThe CIO Area can be used as work bits if no work bits if no used as shown here.)         Link Area         3,200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199 ) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S CS1 CPU Bus Unit 6,400 words): CIO 15000 to CIO 149915 (words CIO 1500 to CIO 1499 9) Area           CS1 CPU Bus Unit         6,400 (400 words): CIO 150000 to CIO 149915 (words CIO 1500 to CIO 1499 9) Area           Special I/O Unit Area         15,360 (960 words): CIO 200000 to CIO 149915 (words CIO 2000 to CIO 2959) CS1 CPU Bus Unit bits are allocated to CS1 special I/O Units (see no (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CIO 19000 to CIO 199915 (words CIO 1900 to CIO 1999 ) Inner Board Dits are allocated to Isave Flacks connected to SNAAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         16,000 (100 words): CIO 190000 to CIO 31311 (Words CIO 00000 to CIO 3131) I/O Terminal bits are allocated to IS145 (words CIO 03000 to CIO 3131) I/O Terminal Area           I/O Terminal Area			
Function Blocks (See note 1.)         Languages supported for use in function block definitions: Ladder programming language and stru- text           CiO (Core I/O) Area         1/O Area         5,120 : CiO 000000 to CiO 031915 (320 words from CiO 0000 to CiO 0319.) Setting of first rack words can be changed from default (CiO 0000) so that CiO 0000 to CiO 0999 used.           Line Lio Area can be used as work bits if not used as shown here.)         3,200 (200 words): CiO 10000 to CiO 119915 (words CiO 1000 to CiO 1199.) Link bits are used for data links and are allocated to Units. C200H Basic I/O Units, and Group-2 High-density I/O Units.           See as shown here.)         CS1 CPU Bus Unit 6,400 (400 words): CiO 150000 to CiO 119915 (words CiO 1500 to CiO 1999.) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S Area           Special I/O Unit         15,360 (960 words): CiO 250000 to CiO 295915 (words CiO 2000 to CiO 2959.) Special I/O Unit bits are allocated to CS1 Special I/O Units and 2200H Special I/O Units. (See not (10 words per Unit, 96 Units max. The maximum number of slots, howerer, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CiO 300000 to CiO 39915 (words CiO 3000 to CiO 399.) Xrea           SYSMAC BUS         800 (50 words): CiO 300000 to CiO 3111 (Words CiO 3000 to CiO 311.) I/O Terminal Area           I/O Terminal Area         512 (32 words): CiO 300000 to CiO 309915 (words CiO 3000 to CiO 3031.) I/O Terminal Area           I/O Terminal Area         1,600 (100 words): CiO 300000 to CiO 309915 (words CiO 3000 to CiO 3030.) CiO 303500			
(See note 1.)         text	Function Blocks		
Area       Setting of first rack words can be changed from default (CIO 0000) so that CIO 0099 used.         IThe CIO Area can be used as work bits if not used as shown bere.)       Setting of first rack words can be changed from default (CIO 0000) so that CIO 0099 (Units, and Group-2 High-density I/O Units, such as CS1 Basic I/O Units, C200H Basic I/O Units, and Group-2 High-density I/O Units.         Ised as shown here.)       Link Area       3,200 (200 words): CIO 150000 to CIO 119915 (words CIO 1500 to CIO 1199)         Link Area       CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (Sew ords per Unit, 16 Units Special I/O Unit is a callocated to CS1 Special I/O Units and C200H Special I/O Units. (See noi (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)         Inner Board Area       1,600 (100 words): CIO 130000 to CIO 39915 (words CIO 1900 to CIO 1999)         Inner Board Area       1,600 (100 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049)         Area       SYSMAC BUS         B00 (50 words): CIO 310000 to CIO 31115 (words CIO 3000 to CIO 30131)       J/O Terminal bits are allocated to 1/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master U words per Alack, 5 Racks max.)         I/O Terminal Area       512 (32 words): CIO 00000 to CIO 039115 (words CIO 0300 to CIO 0311)         J/O Terminal bits are allocated to 1/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master U words per Alack, 5 Racks max.)         I/O O Special I/O <td< td=""><td></td><td></td><td></td></td<>			
(The CIO Area can be used as work bits if not used as shown here.)       UO bits are allocated to Basic I/O Units, such as CS1 Basic I/O Units, C200H Basic I/O Units, and Group-2 High-density I/O Units.         Link Area       3,200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S         CS1 CPU Bus Unit Area       CS1 CPU Bus Unit 5,400 (400 words): CIO 10000 to CIO 29915 (words CIO 1500 to CIO 1899) CS1 CPU Bus Unit 5,360 (960 words): CIO 200000 to CIO 299915 (words CIO 2000 to CIO 2995) Special I/O Unit Area         Special I/O Unit Area       15,360 (960 words): CIO 200000 to CIO 299515 (words CIO 2000 to CIO 2995) Special I/O Unit is are allocated to CS1 Special I/O Units and 2200H Special I/O Units. (See noi (10 words): CIO 100000 to CIO 299515 (words CIO 1900 to CIO 2999) Inner Board Area         Inner Board Area       1,600 (100 words): CIO 300000 to CIO 394915 (words CIO 3000 to CIO 3049) SYSMAC BUS         SYSMAC BUS       800 (50 words): CIO 300000 to CIO 39415 (words CIO 3000 to CIO 3049) SYSMAC BUS bits are allocated to I/O Terminal Units (but not to Siave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)         I/O Terminal Area       1,800 (100 words): CIO 300000 to CIO 039915 (words CIO 00500 to CIO 0399) Systepial I/O Unit bits are allocated to I/O Terminal, 32 Terminals max.)         I/O Terminal Area       512 (32 words): CIO 000000 to CIO 039115 (words CIO 00500 to CIO 0399) DeviceNet I/O Master Units. (1 word per Terminal, 32 Terminals max.)         C200H Special I/O Unit Area       8,196 (512 words): CIO 025000 to C		I/O Area	5,120 : CIO 000000 to CIO 031915 (320 words from CIO 0000 to CIO 0319 )
can be used as work bits if not used as shown here.)       I/O bits are allocated to Basic I/O Units, such as CS1 Basic I/O Units, C200H Basic I/O Units, and Grup-2 High-density I/O Units.         used as shown here.)       Link Area       3,200 (200 words): CIO 1000 to CIO 119915 (words CIO 1000 to CIO 1199) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link Special I/O Unit         Area       CS1 CPU Bus Unit       15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959 )         Special I/O Unit       15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959 )         Special I/O Unit       15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959 )         Special I/O Unit       15,360 (960 words): CIO 190000 to CIO 199915 (words CIO 2000 to CIO 2959 )         Special I/O Unit       16,000 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999 )         Inner Board Area       1,600 (100 words): CIO 10000 to CIO 309015 (words CIO 3000 to CIO 3049 )         SYSMAC BUS       800 (50 words): CIO 30000 to CIO 3011 // Words max.)         SYSMAC BUS bits are allocated to Inner Boards. (100 // Words max.)         I/O Terminal Area       512 (22 words): CIO 310000 to CIO 3131 15 (words CIO 3100 to CIO 3131 )         I/O Terminal Area       512 (32 words): CIO 300000 to CIO 051115 (words CIO 0000 to CIO 0511)         C200H Special I/O       8,196 (512 words): CIO 005000 to CIO 051115 (words CIO 00050 to CIO 0399)         DeviceNe			Setting of first rack words can be changed from default (CIO 0000) so that CIO 0000 to CIO 0999 car
work bits if not used as shown here.)         In our and an advanced shown of the second shown	<b>`</b>		
used as shown here.)         Link Area         3.200 (200 words): CIO 10000 to CIO 119915 (words CIO 1000 to CIO 1199.) Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S           CS1 CPU Bus Unit         6,400 (400 words): CIO 150000 to CIO 189915 (words CIO 1500 to CIO 1899.)           Area         CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (25 words per Unit, 16 Units Special I/O Unit           Area         15,360 (960 words): CIO 200000 to CIO 295915 (words CIO 2000 to CIO 2959.)           Area         Special I/O Unit bits are allocated to CS1 Special I/O Units and C200H Special I/O Units. (See not (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CIO 190000 to CIO 39915 (words CIO 3000 to CIO 3049.)           SYSMAC BUS         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049.)           Area         SYSMAC BUS bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O         8.196 (512 words): CIO 300000 to CIO 039915 (words CIO 0050 to CIO 0399)           Unit Area         1.960 (100 words): Outputs: CIO 005000 to CIO 039915 (words CIO 0050 to CIO 0399)           DeviceNet         1.960 (100 words): Outputs: CIO 005000 to CIO 039915 (words CIO 0050 to CIO 0399)           DeviceNet         1.600 (100 words): Outpu			
Interfect)         Link bits are used for data links and are allocated to Units in Controller Link Systems and PC Link S           CS1 CPU Bus Unit Area         6,400 (400 words): CIO 150000 to CIO 189915 (words CIO 1500 to CIO 1899)           Special I/O Unit Area         15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959)           Special I/O Unit Area         15,360 (960 words): CIO 190000 to CIO 295915 (words CIO 1900 to CIO 1999)           Inner Board Area         1,600 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999)           Inner Board Area         1,600 (100 words): CIO 190000 to CIO 304915 (words CIO 3000 to CIO 3049)           SYSMAC BUS Area         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049)           SYSMAC BUS Area         800 (50 words): CIO 300000 to CIO 31115 (words CIO 3000 to CIO 30491)           I/O Terminal Area         512 (32 words): CIO 300000 to CIO 31115 (words CIO 3100 to CIO 31311)           I/O Terminal Area         512 (32 words): CIO 300000 to CIO 051115 (words CIO 3000 to CIO 05111)           I/O Terminal Area         512 (32 words): CIO 300000 to CIO 051115 (words CIO 3000 to CIO 05111)           Unit Area         512 (32 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 05111)           Unit Area         612 (32 words): CIO 000000 to CIO 059115 (words CIO 0050 to CIO 0591)           C200H Special I/O         8,196 (512 words): CIO 000000 to CIO 059115 (words CIO 0050 to CIO 0599)           PeviceNet Ar		Link Area	
CS1 CPU Bus Unit Area         6,400 (400 words): CIO 150000 to CIO 189915 (words CIO 1500 to CIO 1899 ) CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (25 words per Unit, 16 Units Special I/O Unit Area           Special I/O Unit Area         15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959 ) Special I/O Unit bits are allocated to CS1 Special I/O Units and C200H Special I/O Units. (See not (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999 ) Inner Board bits are allocated to Inner Boards. (100 I/O words max.)           SYSMAC BUS Area         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049 ) SYSMAC BUS bits are allocated to Isave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O         8,196 (512 words): CIO 00000 to CIO 051115 (words CIO 0000 to CIO 0511) (Writ Area         1,600 (100 words): CIO 000000 to CIO 059915 (words CIO 0050 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         4,800 (300 words): CIO 120000 to CIO 025015 (words CIO 02501 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link A	here.)		
Area         CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (25 words per Unit, 16 Units Special I/O Unit           Area         15,360 (960 words): CIO 20000 to CIO 295915 (words CIO 2000 to CIO 2959) Special I/O Unit bits are allocated to CS1 Special I/O Units and C200H Special I/O Units. (See not (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includir expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CIO 19000 to CIO 199915 (words CIO 1900 to CIO 1999 ) Inner Board bits are allocated to Inner Boards. (100 I/O words max.)           SYSMAC BUS         800 (50 words): CIO 30000 to CIO 304915 (words CIO 3000 to CIO 3049 ) SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 30000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate for refreshing.           DeviceNet         1,600 (100 words): CIO 2027400 to CIO 025015 (words CIO 02500 to CIO 05091 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 12000 to CIO 1200 to CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 2030000 to CIO 614315 (words CIO 1200 to CIO 1499) 37,504 (2,344 w		CS1 CPU Bus	
Area         Special I/O Unit bits are allocated to CS1 Special I/O Units and C200H Special I/O Units. (See not (10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 includin expansion slots, so maximum number of Units is actually 80.)           Inner Board Area         1,600 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999 ) Inner Board bits are allocated to Inner Boards. (100 I/O words max.)           SYSMAC BUS         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049 ) Area         SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to V/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate f refreshing.           DeviceNett Area         1,600 (100 words): CIO 027400 to CIO 025015 (words CIO 00500 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 38000 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area			CS1 CPU Bus Unit bits store operating status of CS1 CPU Bus Units. (25 words per Unit, 16 Units ma
Internal I/O Area         Iso of the outs and solution of the outs and solut and the outs and solution of the outs and solut and solutis an		Special I/O Un	it 15,360 (960 words): CIO 200000 to CIO 295915 (words CIO 2000 to CIO 2959 )
Inner Board Area         1,600 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999 ) Inner Board bits are allocated to Inner Boards. (100 I/O words max.)           SYSMAC BUS Area         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049 ) SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMAC Remote I/O Master Units. (1 word per Terminal S Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) U/O Terminal bits are allocated to C200H Special I/O Units and allow access separate f refreshing.           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 03915 (words CIO 0050 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 02500) When a PC Link.           Internal I/O Area         4,800 (300 words): CIO 380000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 1409) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 61433) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external		Area	Special I/O Unit bits are allocated to CS1 Special I/O Units and C200H Special I/O Units. (See note 2
Inner Board Area         1,600 (100 words): CIO 190000 to CIO 199915 (words CIO 1900 to CIO 1999)           Inner Board bits are allocated to Inner Boards. (100 I/O words max.)         SYSMAC BUS           Area         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049)           V/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 31115 (words CIO 3100 to CIO 3131)           I/O Terminal Area         512 (32 words): CIO 3100000 to CIO 051115 (words CIO 00000 to CIO 05111)           C200H Special I/O         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 00000 to CIO 05111)           C200H Special I/O         8,196 (512 words): CIO 000000 to CIO 0051115 (words CIO 0050 to CIO 0059)           C200H Special I/O         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 00500 to CIO 0099)           Internal VArea         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 00500 to CIO 0099)           DeviceNet         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 00500 to CIO 0099)           Inputs:         CIO 027400 to CIO 025015 (words CIO 02505 to CIO 0099)           DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0227 to CIO 0250)           When a PC Link.         4,800 (300 words): CIO 120000 to CIO 14			(10 words per Unit, 96 Units max. The maximum number of slots, however, is limited to 80 including
Inner Board bits are allocated to Inner Boards. (100 I/O words max.)           SYSMAC BUS Area         800 (50 words): CIO 30000 to CIO 304915 (words CIO 3000 to CIO 3049 ) SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate f refreshing.           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Area           DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.           Internal I/O Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			
SYSMAC BUS Area         800 (50 words): CIO 300000 to CIO 304915 (words CIO 3000 to CIO 3049) SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate f refreshing.           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.           Internal I/O Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 149915 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)		Inner Board Ar	
Area         SYSMAC BUS bits are allocated to Slave Racks connected to SYSMAC BUS Remote I/O Master U words per Rack, 5 Racks max.)           I/O Terminal Area         512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131 ) I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) (Unit Area           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 00399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.           Internal I/O Area         4,800 (300 words): CIO 380000 to CIO 614315 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			
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I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMA Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)           C200H Special I/O Unit Area         8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate frefreshing.           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.           Internal I/O Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			
Remote I/O Master Units. (1 word per Terminal, 32 Terminals max.)         C200H Special I/O Unit Area       8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate for refreshing.         DeviceNet Area       1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399)         DeviceNet Area       64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating so other CPU Units in PC Link.         Internal I/O Area       4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)		I/O Terminal A	rea 512 (32 words): CIO 310000 to CIO 313115 (words CIO 3100 to CIO 3131)
C200H Special I/O Unit Area       8,196 (512 words): CIO 000000 to CIO 051115 (words CIO 0000 to CIO 0511) C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate for refreshing.         DeviceNet Area       1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.         PC Link Area       64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.         Internal I/O Area       4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			I/O Terminal bits are allocated to I/O Terminal Units (but not to Slave Racks) connected to SYSMAC E
Unit Area         C200H Special I/O Unit bits are allocated to C200H Special I/O Units and allow access separate frefreshing.           DeviceNet Area         1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399) DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.           PC Link Area         64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.           Internal I/O Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			
DeviceNet       1,600 (100 words): Outputs: CIO 005000 to CIO 009915 (words CIO 0050 to CIO 0099) Inputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399)         DeviceNet       1,600 (100 words): Outputs: CIO 035000 to CIO 039915 (words CIO 0350 to CIO 0399)         DeviceNet bits are allocated to Slaves according to DeviceNet remote I/O communications.         PC Link Area       64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250)         When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.         Internal I/O Area       4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499)         37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143)         These bits in CIO Area are used as work bits in programming to control program execution. They caused for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511)         Control programs only. (I/O from external I/O terminals is not possible.)			
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PC Link Area       64 bits (4 words): CIO 027400 to CIO 025015 (words CIO 0247 to CIO 0250) When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.         Internal I/O Area       4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They ca used for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)		Area	
When a PC Link Unit is used in a PC Link, use these bits to monitor PC Link errors and operating s other CPU Units in PC Link.         Internal I/O Area       4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499) 37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143) These bits in CIO Area are used as work bits in programming to control program execution. They caused for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			<b>v</b>
other CPU Units in PC Link.         Internal I/O Area         4,800 (300 words): CIO 120000 to CIO 149915 (words CIO 1200 to CIO 1499)         37,504 (2,344 words): CIO 380000 to CIO 614315 (words CIO 3800 to CIO 6143)         These bits in CIO Area are used as work bits in programming to control program execution. They caused for external I/O.         Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511)         Control programs only. (I/O from external I/O terminals is not possible.)		PC Link Ar	
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used for external I/O.           Work Area         8,192 bits (512 words): W00000 to W51115 (words W000 to W511) Control programs only. (I/O from external I/O terminals is not possible.)			
Work Area       8,192 bits (512 words): W00000 to W51115 (words W000 to W511)         Control programs only. (I/O from external I/O terminals is not possible.)			
Control programs only. (I/O from external I/O terminals is not possible.)	Work Area		
	HUIN AIGA		
			Trees. When using work bits in programming, use bits in work Area inst before using bits notifi offield

Specifications

of Units

Item	Specification	Lineup o
Holding Area	8,192 bits (512 words): H00000 to H51115 (words H000 to H511)	
-	Holding bits are used to control execution of program, and maintain their ON/OFF status when the PLC is turned OFF or operating mode is changed.	Jnit
	<b>Note:</b> Words H512 to H1535 are allocated to the Function Block Holding Area and are used only for the function block instance area (internally allocated variable area).	CPU Unit Overview
Auxiliary Area	Read only: 7,168 bits (448 words): A00000 to A44715 (words A000 to A447)	
	Read/write: 8,192 bits (512 words): A44800 to A95915 (words A448 to A959)	( E E
	Auxiliary bits are allocated specific functions.	yste
Temporary Area	16 bits (TR00 to TR15) Temporary bits are used to store ON/OFF execution conditions at program branches	fic S
Timer Area	4,096: T0000 to T4095 (used for timers only)	Basic System Configuration
Counter Area	4,096: C0000 to C4095 (used for counters only)	$\rightarrow$
DM Area	32K words: D00000 to D32767	ic sic
	Used as a general-purpose data area for reading and writing data in word units (16 bits). Words in DM Area maintain their status when PLC is turned OFF or operating mode is changed.	Better Basic Performance
	Internal Special I/O Unit DM Area: D20000 to D29599 (100 words × 96 Units). Used to set parameters.	Bet
	CS1 CPU Bus Unit DM Area: D30000 to D31599 (100 words $\times$ 16 Units). Used to set parameters.	
	Inner Board DM Area: D32000 to D32099. Used to set parameters for Inner Boards.	
	The DM Area is a general-purpose data area that is read and written by word (16 bits). The contents of the DM Area is maintained when the PLC is turned OFF or operating mode is changed.	Peripheral Devices
EM Area	32K words per bank, 13 banks max.: E0_00000 to EC_32767 max. (Not available on some CPU Units.)	Pe
	Used as a general-purpose data area for reading and writing data in word units (16 bits). Words in EM Area maintain their status when PLC is turned OFF or operating mode is changed.	
	The EM Area is divided into banks, and addresses can be set by either of following methods.	Jnit
	Changing current bank using EMBC(281) instruction and setting addresses for current bank.	CPU Unit Overview
	Setting bank numbers and addresses directly.	56
	EM data can be stored in files by specifying number of first bank. (EM file memory)	
Data Registers	DR0 to DR15. Store offset values for indirect addressing. Data registers can be used independently in each task. One register is 16 bits (1 word).	ations
Index Registers	IR0 to IR15. Store PLC memory addresses for indirect addressing. Index registers can be used indepen- dently in each task. One register is 32 bits (2 words).	I/O Allocations
Task Flag Area	32 (TK0000 to TK0031). Task Flags are read-only flags that are ON when corresponding cyclic task is ex- ecutable and OFF when corresponding task is not executable or in standby status.	$\rightarrow$
Trace Memory	4,000 words (500 data trace samples at the maximum sample size of 31 bits and 6 words)	Current
File Memory	Memory Cards: Compact flash memory cards can be used (MS-DOS format).	ut I
	EM file memory: Part of EM Area can be converted to file memory (MS-DOS format).	urrer
	OMRON Memory Cards with 15-MB, 30-MB, or 48-MB capacities can be used.	00

Note: 1. Supported for CPU Unit Ver. 3.0 or later only.

2. Up to 16 C200H Special I/O Units can be used in one PLC, and with some C200H Special I/O Units, the limit is 10 Units. There are some I/O Units that are classified as Special I/O Units.

Instructions

Connector Cables

#### ■ Function Specifications

Parallel processing modes	Specification
	The CS1G and CS1H CPU Units support processing modes in which the program is executed in parallel with ripheral processing
Battery-free operation	The user program and system program can be automatically backed up flash memory is mounted.
Constant cycle time	1 to 32,000 ms (Unit: 1 ms)
Cycle time monitoring	Possible (Unit stops operating if cycle is too long): 1 to 40,000 ms (Unit: 10 ms)
I/O refreshing	Cyclic refreshing, immediate refreshing, refreshing by IORF(097).
I/O memory holding when changing op- erating modes	Possible (Depends on ON/OFF status of IOM Hold Bit in Auxiliary Area.)
Load OFF	All outputs on Output Units can be turned OFF.
Input time constant setting	Time constants can be set for inputs from CS1 Basic I/O Units. The time constant can be increased to reduct influence of noise and chattering or it can be decreased to detect shorter pulses on inputs. (CS1 Basic I/O Ur only)
Mode setting at power-up	Possible
Memory Card functions	Automatic reading programs from Memory Card (autoboot).
	Memory Card Storage Data         User program: PC System Setup: I/O Memory:         Program file format (binary) Data file format (binary)           Data file format (binary)         Data file format (binary)
	Memory Card Read/Write User program instructions, Peripheral Devices (such as Programming Console), Host Link computer.
Filing	Memory Card data and EM (Extended Data Memory) Area can be handled as files.
Debugging	Force-set/reset, differential monitoring, data tracing (scheduled, each cycle, or when instruction is executed), struction error tracing.
Online editing	One or more program blocks in user programs can be overwritten when CPU Unit is in PROGRAM or MONITE mode. This function is not available for block programming areas.
Program protection	Overwrite protection:         Set using DIP switch.           Copy protection:         Password set using Peripheral Device.
Error check	User-defined errors (i.e., user can define fatal errors and non-fatal errors) The FPD(269) instruction can be used to check execution time and logic of each programming block.
Error log	Up to 20 errors are stored in error log. Information includes error code, error details, and time error occurred
Serial communications	Built-in peripheral port: Peripheral Device (including Programming Console), Host Links, NT Links Built-in RS-232C port: Peripheral Device (excluding Programming Console), Host Links, no-protocol commun tions, NT Links
	Communications Board (sold separately): Protocol macros, Host Links, NT Links
Clock	Provided on all models.
	Note: Used to store time when power is turned ON and when errors occur.
Power OFF detection time	10 to 25 ms (not fixed)
Power OFF detection delay time	0 to 10 ms (user-defined, default: 0 ms)
Memory protection	Held Areas: Holding bits, contents of Data Memory and Extended Data Memory, and status of counter Comple Flags and present values. Note: If IOM Hold Bit in Auxiliary Area is turned ON, and PC Setup is set to maintain IOM Hold Bit status w
	power to PLC is turned ON, contents of CIO Area, Work Area, part of Auxiliary Area, timer Completion I and PVs, Index Registers, and Data Registers will be saved.
Sending commands to a Host Link com- puter	FINS commands can be sent to a computer connected via Host Link System by executing Network Commun tions Instructions from PLC.
Remote programming and monitoring	Host Link communications can be used for remote programming and remote monitoring through a Controller I System or Ethernet network.
Eight-level communications (See note 2.)	Host Link communications can be used for remote programming and remote monitoring from devices on network up to seven levels away (Controller Link Network, Ethernet Network, or other network).
Storing comments in CPU Unit	I/O comments can be stored in Memory Cards, EM file memory, or in the Comment MemoryÅiSee note 1.) of tained in the CPU Unit's flash memory.
Program check	Program checks are performed at beginning of operation for items such as no END instruction and instruction rors. The CX-Programmer can also be used to check programs.
Control output signals	RUN output: The contacts will turn ON (close) while CPU Unit is operating. These terminals are provided only C200HW-PA204R and C200HW-PA209R Power Supply Units.
Battery life	5 years at 25°C (Depending on the ambient operating temperature and communications conditions, 1.1 years r Battery Set: CS1W-BAT01) (See note 3.)
	CPU errors (watchdog timer), I/O verification errors, I/O bus errors, memory errors, and battery errors.
Self-diagnostics Other functions	Storage of number of times power has been interrupted, the times of the interrupts, and system operation time

### ■ General Specifications

General Spe	cifications					
Item	Specifications					
Power Supply Unit	C200HW-PA204	C200HW-PA204S	C200HW-PA204R	C200HW-PA209R	C200HW-PD024	C200HW-PD106R
Supply voltage	100 to 120 VAC or 200 to 240 VAC, 50/60 Hz				24 VDC	100 VDC
Operating voltage	85 to 132 VAC or	170 to 264 VAC			19.2 to 28.8 VDC	85 to 143 VDC
ange Power consumption	120 VA max.			180 VA max.	40 W max.	50 W max.
Inrush current	30 A max.			30 A max./100 to	30 A max.	50 W max.
				120 VAC 40 A max./200 to 240 VAC		
Output capacity	4.6 A, 5 VDC (inc	cluding CPU Unit po	ower)	9 A, 5 VDC (includ- ing CPU Unit power )		6 A, 5 VDC (includ- ing CPU Unit power
	0.625 A, 26 VDC Total: 30 W	0.625 A, 26 VDC or 0.8 A, 24 VDC Total: 30 W	0.625 A, 26 VDC Total: 30 W	1.3 A, 26 VDC Total: 45 W	0.625 A, 26 VDC Total: 30 W	1 A, 26 VDC Total: 30 W
Output terminal	Not provided	24 VDC load cur- rent consumption Less than 0.3 A: +17%/-11% 0.3 A or greater: +10%/-11% (Lot No. 0197 or higher)	Not provided		Not provided	
RUN output (See Note 1.)	Not provided		Contact configura- tion: SPST-NO Switch capacity: 250 VAC, 2 A (resis- tive load) 250 VAC, 0.5 A (in- duction load), 24 VDC, 2 A	tive load) 120 VAC, 0.5 A (in- ductive load) 24 VDC, 2 A (resis- tive load) 24 VDC, 2 A (induc- tive load)	Not provided	Contact configura- tion: SPST-NO Switch capacity: 250 VAC, 2 A (resis tive load) 250 VAC, 0.5 A (in- ductive load) 24 VDC, 2 A
Insulation resistance	20 $M\Omega$ min. (at 500 VDC) between AC external and GR terminals (See Note 1.)		external and GR terminals (See Note 1.)			
Dielectric strength	<ul> <li>2,300 VAC 50/60 Hz for 1 min between AC external and GR terminals (See Note 1.), leakage current: 10 mA max.</li> <li>1,000 VAC 50/60 Hz for 1 min between AC external and GR terminals (See Note 1.), leakage current: 10 mA max.</li> </ul>			1,000 VAC 50/60 Hz for 1 min between DC external and GR terminals, leak- age current: 10 mA max. (See Note 1.)	2,300 VAC 50/60 Hz for 1 min between DC external and GF terminals, leakage current: 10 mA max (See Note 1.)	
Noise immunity	Conforms to IEC	61000-4-4, 2 kV (po	ower lines)			
Vibration resistance	10 to 57 Hz, 0.075-mm amplitude, 57 to 150 Hz, acceleration: 9.8 m/s <sup>2</sup> in X, Y, and Z directions for 80 minutes (Sweep time 8 min $\times$ 10 = total time 80 min.) CPU Unit mounted to a DIN track: 2 to 55 Hz, 2.9 m/s <sup>2</sup> in X, Y, and Z directions for 20 minutes					
Shock resistance		s each in X, Y, and Z	Z directions			
Ambient operating	0 to 55°C					
emperature Ambient operating numidity	10% to 90% (with no condensation)					
Atmosphere	Must be free from corrosive gases.					
Ambient storage	-20 to 75°C (exc	-				
Grounding	Less than 100 $\Omega$					
Enclosure	Less than 100 Ω Mounted in a panel.					

Item

Specifications

10	
CPU Unit	Overview

Basic System Configuration

Peripheral Devices

38	

nem	Specifi	cations				
Weight	All models are each 6 kg max.					
CPU Rack Dimen-	2 slots: 198.5 × 157 × 123 (W x H x D)					
sions (mm)	3 slots: 260 × 130 × 123 (W x H x D)					
(See note 3.)	5 slots: 330 × 130 × 123 (W x H x D)					
	8 slots: 435 × 130 × 123 (W x H x D) 10 slots: 505 × 130 × 123 (W x H x D)					
Safety measures	Conforms to UL, CSA, cULus, cUL, NK, Llovd's, and EC dire	ectives. Conforms to cULus				
	· · · · · · · · · · · · · · · · · · ·					
nents will be d 2. Only when mo 3. Depth is 153 n	Power Supply Unit's LG terminal from the GR terminal who amages if testing is performed with these terminals connecte unted to a Backplane. In for C200HW-PA209R. ately for general specifications of Process I/O Units.					
Item	Specifi	cations				
Power Supply Unit	CS1D-PA207R	CS1D-PD024				
Supply voltage	100 to 120 VAC or 200 to 240 VAC, 50/60 Hz	24 VDC				
Operating voltage	85 to 132 VAC or 170 to 264 VAC	19.2 to 28.8 VDC				
range						
Power consumption	150 VA max.	40 W max.				
Inrush current	30 A max. at100 to 120 VAC, 40 A max. at 200 to 240 VAC	30 A max.				
Output capacity	7 A, 5 VDC (including CPU Unit power)	4.3 A, 5 VDC (including CPU Unit power)				
	1.3 A, 26 VDC	0.56 A, 26 VDC				
	Total: 35 W	Total: 28 W				
Output terminal	Not provided					
RUN output	Contact configuration: SPST-NO	Not provided				
(See Note 2.)	Switch capacity:					
	240 VAC, 2 A (resistive load)					
	120 VAC, 0.5 A (inductive load) 24 VDC, 2 A (resistive load)					
	24 VDC, 2 A (inductive load)					
Insulation resistance	20 M $\Omega$ min. (at 500 VDC) between AC external and GR ter-	20 M $\Omega$ min. (at 500 VDC) between DC external and GR ter				
	minals (See Note 2.)	minals (See Note 2.)				
Dielectric strength	2,300 VAC 50/60 Hz for 1 min between AC external and GR	1,000 VAC 50/60 Hz for 1 min between DC external and GR				
	terminals (See Note 2.)	terminals, leakage current: 10 mA max. (See Note 2.)				
	Leakage current: 10 mA max.					
	1,000 VAC 50/60 Hz for 1 min between AC external and GR					
	terminals (See Note 2.)					
	Leakage current: 10 mA max.					
Noise immunity	Conforms to IEC61000-4-4, 2 kV (power lines)					
Vibration resistance	10 to 57 Hz, 0.075-mm amplitude, 57 to 150 Hz, acceleration: 9.8 m/s <sup>2</sup> in X, Y, and Z directions for 80 minutes (Sweep time 8 min $\times$ 10 = total time 80 min.)					
Shock resistance	147 m/s <sup>2</sup> , 3 times each in X, Y, and Z directions					
Ambient operating temperature	0 to 55°C					
Ambient operating humidity	10% to 90% (with no condensation)					
Atmosphere	Must be free from corrosive gases.					
Ambient storage tem- perature	-20 to 75°C (excluding battery)					
	Less than 100 $\Omega$					
Grounding	Less man 100 22					
Grounding Enclosure	Mounted in a panel.					

Specifications

Note: 1. Only when mounted to a CPU Backplane.

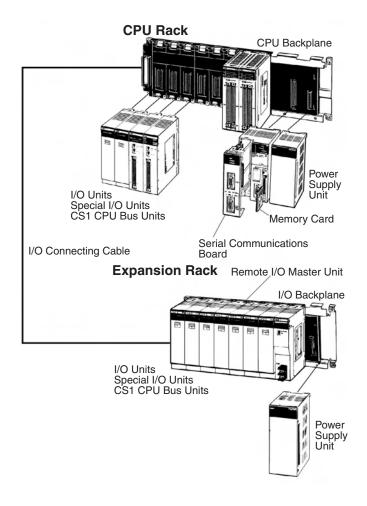
2. Disconnect the Power Supply Unit's LG terminal from the GR terminal when testing insulation and dielectric strength. Internal components will be damages if testing is performed with these terminals connected.

# Lineup of Units

CPU Unit Overview

Peripheral Devices

## **CS1G/CS1H Basic System Configuration**



#### ■ CPU Rack

A CPU Rack consists of a CPU Unit, Power Supply Unit, CPU Backplane, Basic I/O Units, Special I/O Units, and CPU Bus Units. The Serial Communications Board and Memory Cards are optional.

Note: The Backplane depends on the type of CPU Rack, Expansion I/O Racks, and Slave Racks that are used.

#### Expansion Racks

Both C200H and CS1 Expansion Racks can be used.

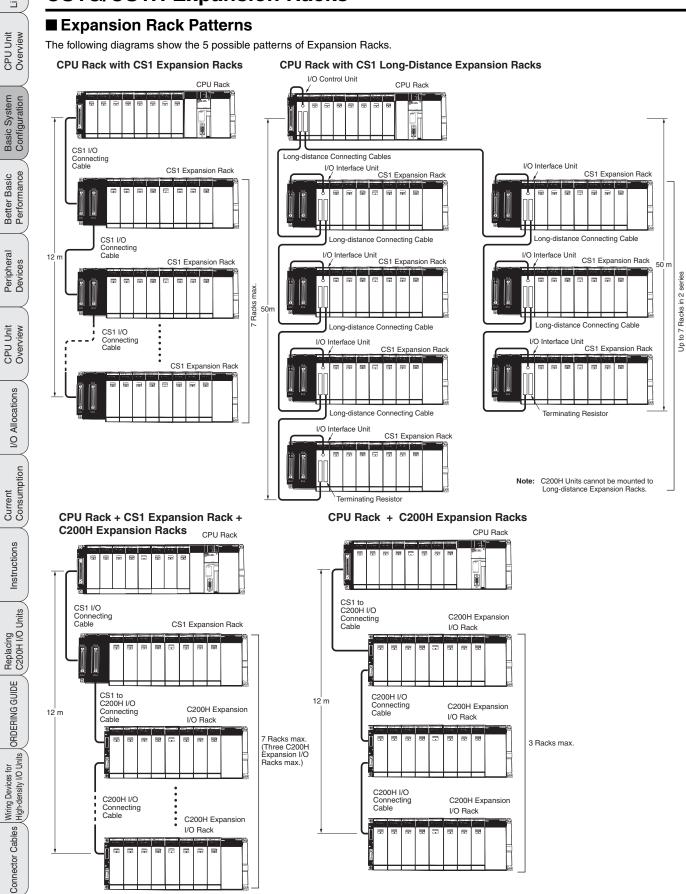
- C200H Expansion I/O Racks can be connected to CPU Racks, CS1 Expansion Racks, or other C200H Expansion I/O Racks.
- · CS1 Expansion Racks can be connected to CPU Racks or other CS1 Expansion Racks.

An Expansion Rack consists of a Power Supply Unit, a CS1 or C200H Expansion I/O Backplane, Basic I/O Units, Special I/O Units, and a CS1 CPU Bus Units.

### ■ Long-distance Expansion Racks

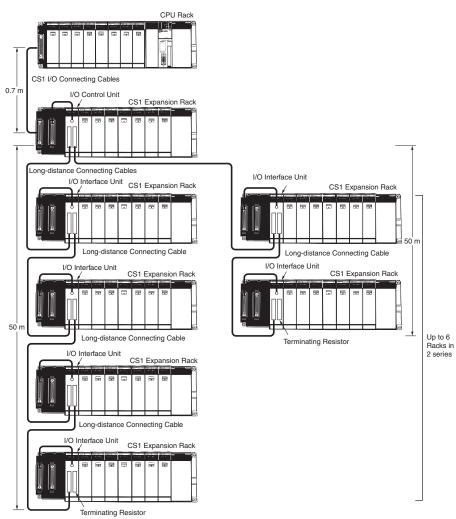
An I/O Control Unit and I/O Interface Units can be used to extend the normal limit of 12 m to 50 m for each of two series of CS1 Expansion Racks. The following Units can be mounted to Long-distance Expansion Racks: CS1 Basic I/O Units, CS1 Special I/O Units, and CS1 CPU Bus Units. (C200H Units cannot be mounted to Long-distance Expansion Racks.)

## **CS1G/CS1H Expansion Racks**



CS1G/CS1H Expansion Racks

CPU Rack with CS1 Expansion Rack and CS1 Long-Distance Expansion Racks

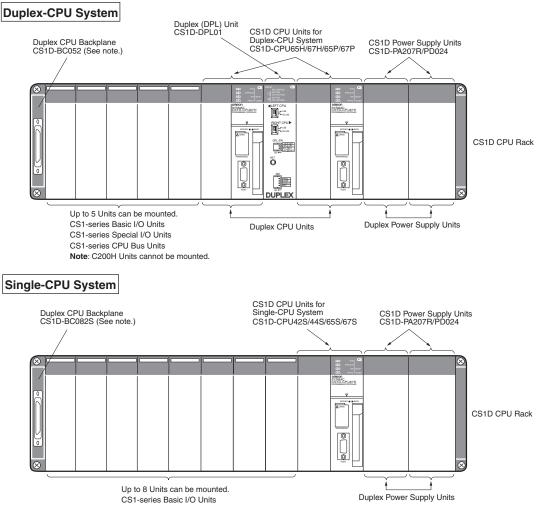


Note: C200H Units cannot be mounted to Long-distance Expansion Racks. (They can be mounted to the CS1 Expansion Rack with the I/O Control Unit mounted.)

Lineup of Units

CPU Unit Overview

## **CS1D System Configuration**



CS1-series Special I/O Units CS1-series CPU Bus Units

Note: C200H Units cannot be mounted

#### **CPU Rack**

A CPU Rack consists of a Duplex CPU Backplane to which CPU Units, Power Supply Units, a Duplex Unit, CS1-series Basic I/O Units, CS1-series Special I/O Units, and CS1-series CPU Bus Units are mounted.

Memory Cards and Inner Boards to mount in the CPU Units are optional. (Inner Board cannot be mounted to the CS1D-CPU H/P) The CPU Units, Power Supply Units, Duplex CPU Backplane, and Duplex Unit are all designed specifically for CS1D PLCs.

Note: Different Backplanes are used for the CPU Rack and Expansion Racks. Be sure to use the correct Backplane.

#### Expansion Racks

An Expansion Rack consists of an Expansion Backplane to which Power Supply Units, CS1-series Basic I/O Units, CS1-series Special I/O Units, and CS1-series CPU Bus Units are mounted.

The Power Supply Units and Expansion Backplane are designed specifically for CS1D PLCs.

CS1-series Expansion Backplanes and C200H Backplanes cannot be connected.

#### Long-distance Expansion Racks

A Long-distance Expansion Rack consists of an Expansion Backplane to which an I/O Interface Unit, CS1-series Basic I/O Units, CS1-series Special I/O Units, and CS1-series CPU Bus Units are mounted. An I/O Control Unit is used to connect to the Long-distance Expansion Racks.

Using Long-distance Expansion Rack increases the normal limit of 12 m for the Rack to 50 m.

#### CS1D PLCs

With a CS1D Duplex-CPU System, two CPU Units can be mounted to the CPU Rack for Duplex Mode operation (Duplex Mode), or just one CPU Unit can be mounted for Simplex Mode operation. In either case, a Duplex Unit is required.

With a CS1D Single-CPU System, just one CPU Unit is mounted and a Duplex Unit is not required.

Also, two Power Supply Units can be mounted to any Rack to increase redundancy. (Racks can also be operated with only one Power Supply Unit.) With any of these combinations, there are no further restrictions if the system configuration, e.g., the same number of Expansion Racks can be used as with the other CS1-series PLCs.

Note: C200H Basic I/O Units, C200H Special I/O Units, and C200H CPU Bus Units cannot be mounted on any Rack.

Basic System Configuration

Instructions

Replacing C200H I/O Units

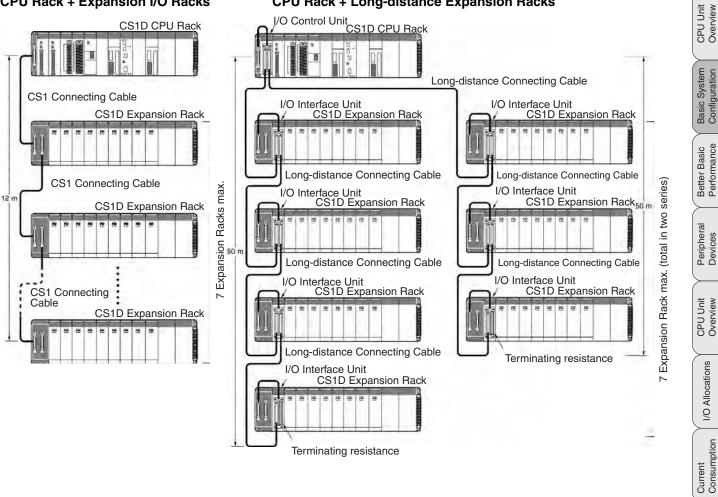
**ORDERING GUIDE** 

Wiring Devices for High-density I/O Units

CPU Unit Overview

Lineup of Units

# CS1D Expansion Rack Patterns There are two patterns that can be used. CPU Rack + Expansion I/O Racks CPU Rack + Long-distance Expansion Racks CS1D CPU Rack



Peripheral Devices

Instructions

Replacing C200H I/O Units

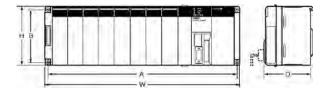
**ORDERING GUIDE** 

Wiring Devices for High-density I/O Units

Unit: mm

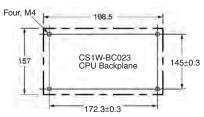
## **Mounting Dimensions**

#### Dimensions



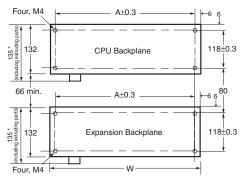
#### Backplanes

#### CPU Backplane with 2 Slots



Note: Expansion Backplanes cannot be connected to 2-slot CPU Backplanes.

#### CPU Backplane with 3, 5, 8, or 10 Slots



\* The CS1D Backplane does not have any extruding parts.

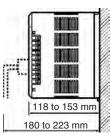
Unit: mm

Backplane	Model	Α	W
CPU Backplanes	CS1W-BC022/023 (2 slots)	172.3	198.5
	CS1W-BC032/033 (3 slots)	246	260
	CS1W-BC052/053 (5 slots)	316	330
	CS1W-BC082/083 (8 slots)	421	435
	CS1W-BC102/103 (10 slots)	491	505
	CS1D-BC052 (for Duplex-CPU Systems)		
	CS1D-BC082S (for Single-CPU System)		
CS1 Expansion	CS1W-BI032/033 (3 slots)	246	260
Backplanes	CS1W-BI052/053 (5 slots)	316	330
	CS1W-BI082/83 (8 slots)	421	435
	CS1W-BI102/103 (10 slots)	491	505
	CS1D-BI092 (for CS1D PLC)	1	
C200H Expan-	C200HW-BI031 (3 slots)	175	189
sion I/O Back- planes	C200HW-BI051 (5 slots)	245	259
	C200HW-BI081-V1 (8 slots)	350	364
	C200HW-BI101-V1 (10 slots)	420	434

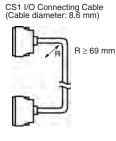
Backplane	Α	В	w	н	D
CS1W-BC022/023 (2 slots)	172.3	145	198.5	157	123
CS1W-BC032/033 (3 slots)	246	118	260	132	
CS1W-BC052/053 (5 slots)	316	1	330		
CS1W-BC082/083 (8 slots)	421	1	435		
CS1W-BC102/103 (10 slots)	491		505		
CS1D-BC052 (for Duplex- CPU System)					
CS1D-BC082S (for Single- CPU System)					

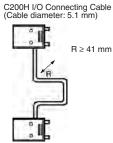
#### Mounting Height

The height of all Racks is from 118 to 153 mm depending on the Units that are mounted. Additional height is required to connect Peripheral Devices and Cables. Be sure to allow sufficient mounting height in the control panel containing the PLC.

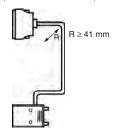


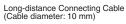
Note: I/O Connecting Cables are 12 m long max. and require sufficient space to maintain the min. bending radius.

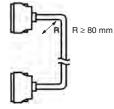




CS1 to C200H I/O Connecting Cable (Cable diameter: 5.1 mm)







CPU Unit Overview

Basic System Configuration

Better Basic Performance

Peripheral Devices

CPU Unit Overview

I/O Allocations

Current Consumption

Instructions

Replacing C200H I/O Units

Wiring Devices for High-density I/O Units

Connector Cables

#### CS1-series Features

# Lineup of Units

Peripheral Devices

# **CS1-series Features**

## Better Basic Performance

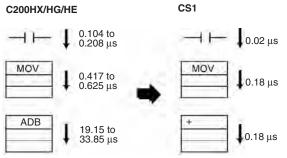
Large Program, Memory, and I/O Capacity; High-speed Instructions and Peripheral Servicing

#### Better Machine Performance with High-speed Processing

CS1 PLCs provide ample speed for advanced machine interfaces, communications, and data processing.

#### **Execution Times from 20 ns**

Faster instruction processing includes 0.02  $\mu$ s for LD and 0.18  $\mu$ s for MOV. And special instructions are processed almost as fast as basic ones (e.g., as fast as 0.18 µs for some instructions).

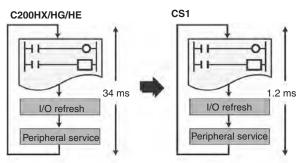


#### 30 Times the Overall Cycle Speed

CS1 refresh time for 96 input points:

For 96 output points:

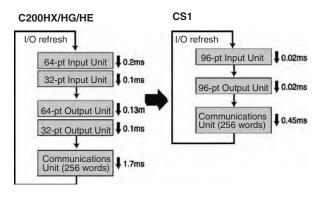
The following examples are for 30K-step programs (basic instructions: 50%; MOV instructions: 30%; arithmetic operation instructions: 20%).



#### 4 Times the Peripheral Servicing and I/O Refresh Speed

0.02 ms (15 times faster) 0.02 ms (10 times faster)

For 256 words for Communications Unit: 0.45 ms (4 times faster)

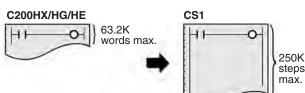


## Large Capacities to Do the Job

CS1 PLCs also provide ample capacity for advanced machine interfaces, communications, and data processing.

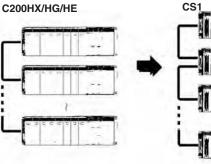
#### 4 Times the Program Capacity

Create programs with up to 250K steps.



#### 4.3 Times the I/O Capacity

Handle up to 5,120 I/O points.

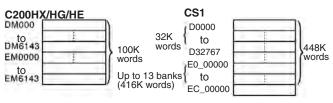


5,120 I/O points

#### 4.5 Times the Data Memory

1,184 I/O points

Use up to 448K words of data memory (word data).



#### 16 Times the Number of Timers/Counters

Program up to 4,096 timers and 4,096 counters.

